



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Abadeer et al.

Serial No.: 10/605,466

Filed: October 1, 2003

Title: Voltage Divider for Integrated Circuits

Group Art Unit: 2816

Examiner: Terry D. Cunningham

Attorney Docket No.: BUR920030053US1
(02016-00160)

CERTIFICATE OF MAILING (37 C.F.R. 1.8a)

I hereby certify that this correspondence is, on the date shown below, being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, Alexandria, VA 22313-1450.

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February 28, 2005

TRANSMITTAL OF
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §1.97(b)

In connection with the above-identified application, please find attached a supplement to the Information Disclosure Statement filed on October 1, 2003, along with a copy of the reference cited therein.

The Information Disclosure Statement transmitted herewith is being filed after the mailing date of a first Office Action on the merits. Therefore, a fee in the amount of \$180 is due in connection with this submission.

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